



SRI VENKATESWARA COLLEGE OF ENGINEERING

(AUTONOMOUS)

Karakambadi Road, Tirupati-517507

R24

EXAMINATION BRANCH

M.Tech I Semester (R24) Supplementary Examinations Feb-2026 TIME TABLE

Exam Timings: **10.00 AM TO 1.00 PM**

Date/Day	VLSI Design (VLSI D)	Computer Science and Engineering (CSE)
02-02-2026 (Monday)	CMOS Analog IC Design EC24DPC101	Advanced Data Structures and Algorithms CS24DPC101
04-02-2026 (Wednesday)	CMOS Digital IC Design EC24DPC102	Advanced Operating Systems CS24DPC102
06-02-2026 (Friday)	CAD for VLSI EC24DPE101	Machine Learning CS24DPE101
09-02-2026 (Monday)	CPLD and FPGA Architectures and Applications EC24DPE105	Data Science CS24DPE104
11-02-2026 (Wednesday)	Research Methodology and IPR EC24DRM101	

Y.A. Givansed
Controller of Examinations

Date: 20-01-2026

[Signature]
Principal